

10/21/10

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER .

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>KS</p> <p>SCANNED MS 4 Q.A. <i>[Signature]</i></p>	<p>PATENT DATE</p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/761253	F	327	43	2816	Tra

APPLICANTS
Zhenhua Wang

TITLE
Circuit for voltage level detection

PTO-2040
12/99

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
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